

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("6890987").PN.	US-PGPUB; USPAT	OR	OFF	2006/07/07 12:53
S1	10	((("6,282,636") or ("6,412,062") or ("7,055,060") or ("20020116438") or ("20030005380") or ("20040015669") or ("20040111566") or ("20040153611") or ("20060112238") or ("20060136925"))).PN.	US-PGPUB; USPAT	OR	OFF	2006/07/06 15:46
S2	512282	multi-core processor\$1 or processor core\$1 or multi-processor core\$1 or multi-core\$1 or core\$1	US-PGPUB; USPAT	ADJ	ON	2006/07/06 15:47
S3	454	test access port controller\$1 or TAPC	US-PGPUB; USPAT	ADJ	ON	2006/07/06 15:48
S4	9556	distributed data	US-PGPUB; USPAT	ADJ	ON	2006/07/06 15:48
S5	21308	control registers	US-PGPUB; USPAT	ADJ	ON	2006/07/06 15:48
S6	8	test control mechanism	US-PGPUB; USPAT	ADJ	ON	2006/07/06 15:48
S7	1	S6 and (S4 same S5)	US-PGPUB; USPAT	ADJ	ON	2006/07/06 15:49
S8	1	S6 and (S4 and S5)	US-PGPUB; USPAT	ADJ	ON	2006/07/06 15:49
S9	251	S4 and S5	US-PGPUB; USPAT	ADJ	ON	2006/07/06 15:49
S10	1	S9 and S3	US-PGPUB; USPAT	ADJ	ON	2006/07/06 15:49
S11	68	S2 and S9	US-PGPUB; USPAT	ADJ	ON	2006/07/06 15:49
S12	15784	test control\$3	US-PGPUB; USPAT	ADJ	ON	2006/07/06 15:49
S14	10998	intergrated test bus or (ITB)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/06 15:50

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S15	3	S11 and S14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/06 16:02
S16	5363	714/726 or 714/30 or 714/724 or 714/725 or 714/733 or 714/734	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 12:52

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1	(multi-core processor AND test control mechanism AND test access port controller AND (TAPC) AND distributed data AND control registers AND integrated test bus AND (ITB)).clm.	US-PGPUB; USPAT	ADJ	ON	2006/07/07 12:56

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1	(multi-core processor AND test control mechanism AND test access port controller AND (TAPC) AND distributed data AND control registers AND integrated test bus AND (ITB)).clm.	US-PGPUB; USPAT	ADJ	ON	2006/07/07 12:56
L3	5363	714/726 or 714/30 or 714/724 or 714/725 or 714/733 or 714/734	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 13:02
L4	512283	multi-core processor\$1 or processor core\$1 or multi-processor core\$1 or multi-core\$1 or core\$1	US-PGPUB; USPAT	ADJ	ON	2006/07/07 13:03
L5	454	test access port controller\$1 or TAPC	US-PGPUB; USPAT	ADJ	ON	2006/07/07 13:03
L6	9556	distributed data	US-PGPUB; USPAT	ADJ	ON	2006/07/07 13:03
L7	21308	control registers	US-PGPUB; USPAT	ADJ	ON	2006/07/07 13:03
L8	8	test control mechanism	US-PGPUB; USPAT	ADJ	ON	2006/07/07 13:03
L9	15784	test control\$3	US-PGPUB; USPAT	ADJ	ON	2006/07/07 13:04
L10	10998	intergrated test bus or (ITB)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 13:08
L11	146	I4 and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 13:08
L12	1	I11 and I6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 13:09

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L13	1	I11 and I10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 13:08
L14	44	I11 and I7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 13:09
L15	28	I14 and I9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 13:09
L16	1	I15 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 13:09
L17	15	I15 and I3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 13:51
L18	180	I10 and (I4 or I5 or I6 or I7 or I9)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 13:52
L19	3	I18 and I3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/07 13:52